UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,188,209 B2
APPLICATION NO. : 10/827117
DATED : March 6, 2007
INVENTOR(S) : Petty et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the title page, section entitled "Related U.S. Application Data," subsections (63) and (60) should read:

(63) Continuation-in-part of application No. 10/802,532, filed on Mar. 16, 2004.

(60) Provisional application No. 60/555,127, filed on Mar. 22, 2004, provisional application No. 60/541,673, filed on Feb. 4, 2004, provisional application No. 60/523,522, filed on Nov. 19, 2003, provisional application No. 60/515,558, filed on Oct. 29, 2003, provisional application No. 60/491,314, filed on Jul. 30, 2003, provisional application No. 60/464,382, filed on Apr. 18, 2003.

Column 2, lines 15-35 should read:

The three aforementioned co-pending U.S. patent applications (i.e., Ser. Nos. 101757,713, 101757,711, and 101757,714) claim the benefit of the following U.S. Provisional Applications:

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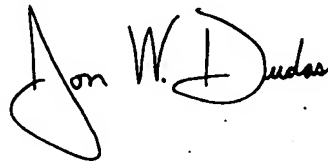DATED   : March 6, 2007
INVENTOR(S)   : Petty et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

SERIAL NUMBER	FILING DATE	TITLE
60/440788 (NEXTIO.0101)	Jan. 21, 2003	SHARED IO ARCHITECTURE
60/440789 (NEXTIO.0102)	Jan. 21, 2003	3GIO-XAUI COMBINED SWITCH
60/464382 (NEXTIO.0103)	Apr. 18, 2003	SHARED-IO PCI COMPLIANT SWITCH
60/491314 (NEXTIO.0104)	Jul. 30, 2003	SHARED NIC BLOCK DIAGRAM
60/515558 (NEXTIO.0105)	Oct. 29, 2003	NEXIS
60/523522 (NEXTIO.0106)	Nov. 19, 2003	SWITCH FOR SHARED I/O FABRIC

Signed and Sealed this

Sixteenth Day of December, 2008

JON W. DUDAS
Director of the United States Patent and Trademark Office